



<http://www.montblanc-project.eu>

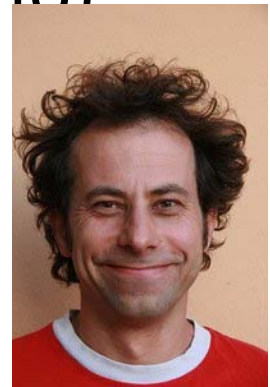
Industrial End-User Group (IUG)

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Objective of IUG

- To **validate the project's novel technologies and provide feedback** to the projects in order to align its objectives, deliverables and address issues such as end-user compatibility.
- An **Industrial End-User Group** Coordinator has been appointed to coordinate this process.
- The IUG will consist of representatives from various industries, including (but not limited to) Automotive, Energy, Oil/Gas, Aerospace, Pharmaceutical and Financial.



Process - 2

- In particular, the tasks of IUG members will be requested to carry out the following tasks:
 - **Install their software and applications on at least one test platform** (it is assumed that IUG members have the right to use that software, we will provide user isolation so that code and data are not shared across users)
 - Perform **evaluations** on at least one platform: execution time, scalability, energy consumption
 - Use the provided **performance analysis tools** to detect performance or scalability issues and bottlenecks (extrae+paraver, score-p+scalasca)
 - Attend the **yearly IUG meeting** and **liaise** with the IUG coordinator on the above
 - **Report** on these evaluations to the consortium at the IUG meeting (this could be a presentation, or a written report; both following a template that we will provide)

Process - 1

In order to complete this task, the Mont-Blanc project will provide IUG members with:

- **Terms and Conditions**
- **Remote access** to Mont-Blanc prototype platforms
- **User support** regarding use of the platforms and software environment
- User support regarding evaluation of the platforms and performance analysis

Platforms

The platforms currently available include:

- **72-node cluster of Quad-core ARM Cortex-A9 processors, 2 GB of DDR3 per node, 1 GbE interconnect**
- **3-node cluster of Dual-core ARM Cortex-A15 processors + ARM Mali-T604 GPU accelerator, 2 GB of DDR3 per node, 1 GbE interconnect**
- (This is expected to grow to 50-100 nodes with 4GB of DDR3 before June'14, and to 800-1000 nodes before October'14 (Mont-Blanc prototype))
- **24-node cluster of Quad-core ARM Cortex-A15 processors, 3 GB of DDR3 per node, 1 GbE interconnect**

Status

